Lab 02 task

1. Make the circuit in the software and verify the truth table and attached the snap shots of the output which you have performed on the LOGIC WORK software, this task done for all the basic gates.
2. Make the ciruit diagram of 2 input AND gate and (1 inv) and 2 input OR (1 inv), make a truth table and verify the results.
3. Make the circuit on software of three input AND, OR gate and write the truth table of AND, OR gate and verify the results at the software.

*You may make TT on copy and scan the image then convert into PDF or make it on word file. It’s your choice what should be convenient for you guys.* ☺